

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	189	((integrated adj circuit\$2) or circuit\$2) with (print adj head) with (support chassis brace frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/27 17:08
S2	64140	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 15:42
S3	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 15:53
S4	73	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 15:42
S5	557	((two four eight sixteen) near15 circuit) with control\$3) and (print adj head)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 10:13
S6	64198	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 09:56
S7	238	S6 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 09:56
S8	142671	((two four eight sixteen) near15 circuit) with control\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 10:13

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S9	993	S8 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 10:13
S10	25	("4423401" "4553393" "4672398" "4706130" "4737802" "4855567" "4864824" "4947192" "5029805" "5057854" "5107276" "5160945" "5218754" "5258774" "5412410" "5565900" "5665249" "5666141" "5719602" "5719604" "5812159" "5867183" "5896155" "6007187" "6312114").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/01 10:16
S11	127749	controller with circuit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/01 10:16
S12	4	S11 and S10	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/01 10:17
S13	0	("6916082").URPN.	USPAT	OR	OFF	2005/12/01 10:21
S14	464	(347/42).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 09:27
S15	74622	flexible with circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 15:53
S16	64198	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 15:54
S17	2402	S15 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 15:54

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S18	189	((integrated adj circuit\$2) or circuit\$2) with (print adj head) with (support chassis brace frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 15:54
S19	28	S17 and S18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/02 15:54
S20	2	("20050157046").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/27 17:08
S21	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/27 17:16
S22	1	("7021843").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/27 17:20
S23	250	"347"/\$.ccls. and (connect\$4 with receiv\$4 with (circuit\$3 chip)) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 17:22
S24	0	"347"/\$.ccls. and (connect\$4 with receiv\$4 with (circuit\$3 near board near flex\$4)) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 17:23
S25	0	"347"/\$.ccls. and (connect\$4 with receiv\$4 with (circuit\$3 near5 flex\$4)) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 17:23

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S26	51	"347"/\$.cls. and (connect\$4 with receiv\$4 with port with (circuit\$3)) and Silverbrook.IN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/27 17:24
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